

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10056193	01/24/2002	365	65	2848	V. Nguyen

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\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

2008.003800

TITLE : Memory module with integrated bus termination

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED

Amount Due Date Paid

ISSUE FEE

Amount Due Date Paid



TERMINAL

DISCLAIMER

Assistant Examiner

Primary Examiner

PREPARED FOR ISSUE

CLAIMS ALLOWED

Total Claims

Print Claim for  
O.G

DRAWING

Sheets Drwg.

Figs. Drwg.

Print Fig.

Application Examiner

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☐ CD-ROM

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